

## ABSTRACT

The light-emitting thyristor matrix array may be provided in which wiring are crossed without being electrically connected to each other. An array of three-terminal light-emitting thyristors in which a substrate is used as a common cathode or anode is divided into blocks  $n$  by  $n$  ( $n$  is an integer  $\geq 2$ ), gates of  $n$  light-emitting thyristors included in each block are separately connected to  $n$  gate-selecting lines, and anodes or cathodes of  $n$  light-emitting thyristors included in each block are commonly connected to one terminal, respectively.

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